

23

Notice of Allowability

Application No.

10/571,191

Examiner

Hien N. Nguyen

Applicant(s)

LOEW, ANDREAS

Art Unit

2824

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 3/9/06.
2. ☒ The allowed claim(s) is/are 1,2 and 4-9.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date 3/9/06
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☒ Examiner's Amendment/~~Comment~~
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☒ Other Search Report & Attachment A.

RICHARD T. ELMS
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

DETAILED ACTION

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the claim;

- In claim 5, "The circuit of claim 3" have been changed to ---The circuit of claim 4---.

(Claim 3 was cancelled by the applicant). See Attachment A.

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on 3/09/06 have being considered by the examiner.

Allowable Subject Matter

3. Claims 1-2 and 4-9 are allowed over prior art of references.
4. The following is an examiner's statement of reasons for allowance:


The primary reason for allowance is for at least the inclusion of a limitation of *"input data can be written at individual write addresses at a first clock rate and output data can be read at different read addresses at a second clock rate, wherein a write reset signal is supplied to a circuit which reset signal resets the write address to an initial value, wherein a read reset signal is supplied to the circuit, which read reset signal resets the read address to an initial value"* with a flip-flop and a gate (switching means) are arranged in a predetermined manner to produce *"a fixed temporal relationship of a read-out data with regard to a read-side start signal"* as disclosed in claim 1. The circuit will be able to produce two reset pulses which cannot occur simultaneously in a FIFO circuit.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hien N. Nguyen whose telephone number is (571) 272-1879. The examiner can normally be reached on Monday through Thursday 9:30 AM to 7:00 PM..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2824

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

H. Nguyen 
May 11, 2007

Listing and Amendments to the Claims

This listing of claims will replace the claims that were published in the PCT Application:

1. (currently amended) A circuit for addressing a memory, to which memory, independently from each other, input data can be written at different individual write addresses with at a first clock rate and from which output data can be read at different read addresses with at a second clock rate, it being possible for the memory to be fed wherein a write reset pulse that signal is supplied to the circuit, which reset signal resets the write address to an initial value, and it being possible for the memory to be fed wherein a read reset pulse that signal is supplied to the circuit, which read reset signal resets the read address to an initial value, wherein switching means are provided in order to, which derive the read reset pulse signal from the write reset pulse signal, and wherein the circuit includes an adjustable delay element, producing a fixed temporal relationship of the read-out data with regard to a read-side start signal.

2. (currently amended) The circuit as claimed in claim of claim 1, wherein the circuit ~~comprises~~ includes a detector ~~set up in order~~ adapted to detect synchronization data from the input data ~~in order to generate~~ for generating the write reset ~~pulse signal~~.

Claim 3 is cancelled.

4. (currently amended) The circuit ~~as claimed in claim 3~~ of claim 1, wherein the circuit ~~has~~ includes a counter that is started by in dependence of the start pulse signal and, beginning from a start value, counts down proceeding from a start value as far as to an end value.

5. (currently amended) The circuit ~~as claimed in~~ of claim ⁴ ~~[4]~~ 3, wherein the circuit ~~is provided with~~ includes a storage means, in which the present value of the counter is stored if a read-side reset ~~pulse occurs~~ signal is present.

(ATTACHMENT A)

HN
5/11/07